Fig.1

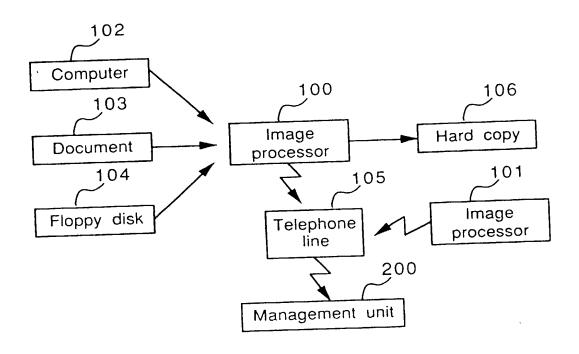


Fig.2

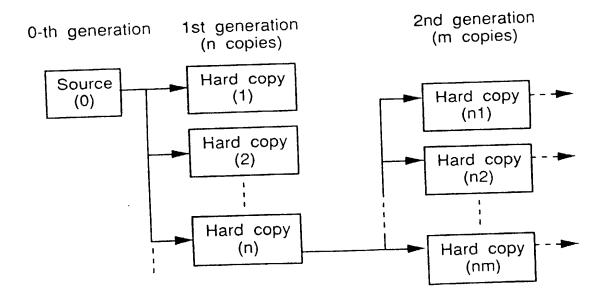
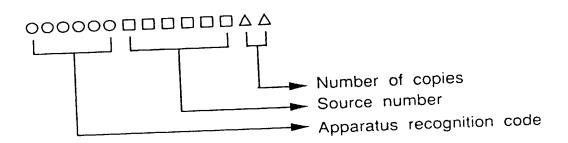


Fig.3

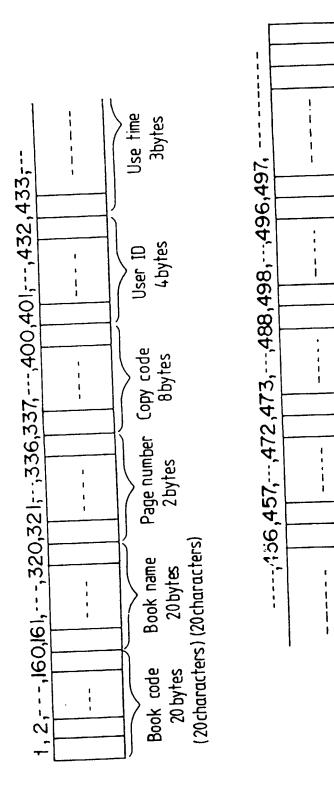


Generation code

> generation number

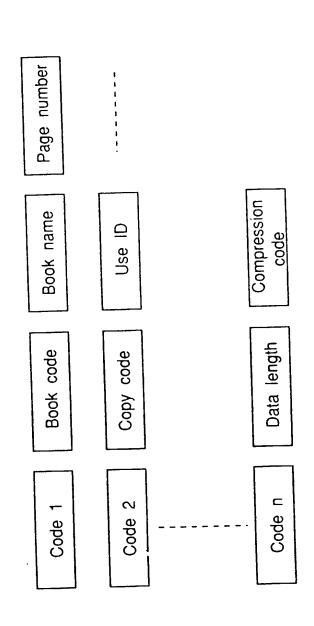
Number of total pxels

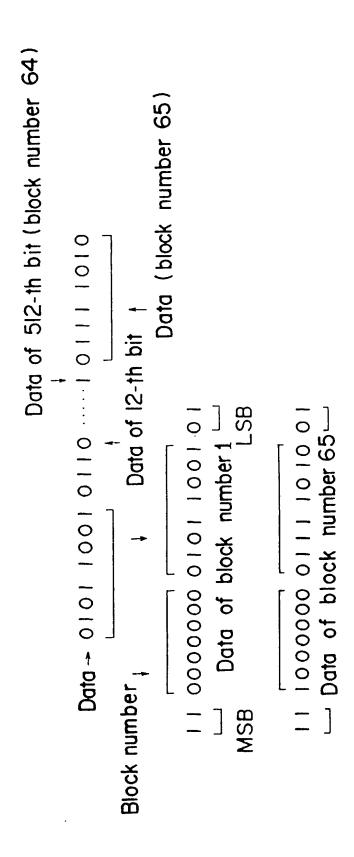
Password number



F19. 4

Fig.5





F19. 6

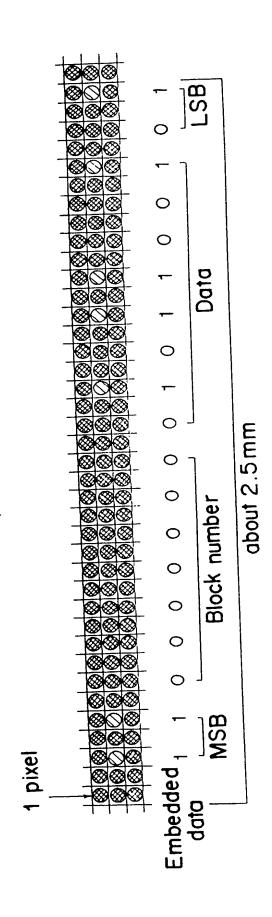
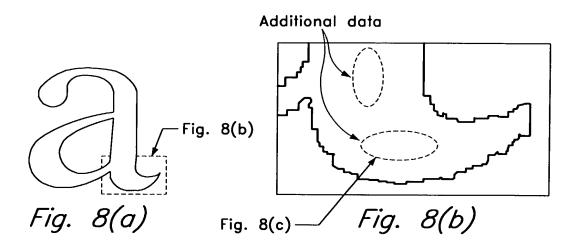
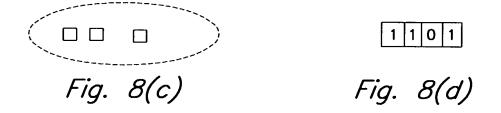
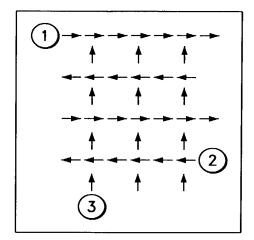


Fig.

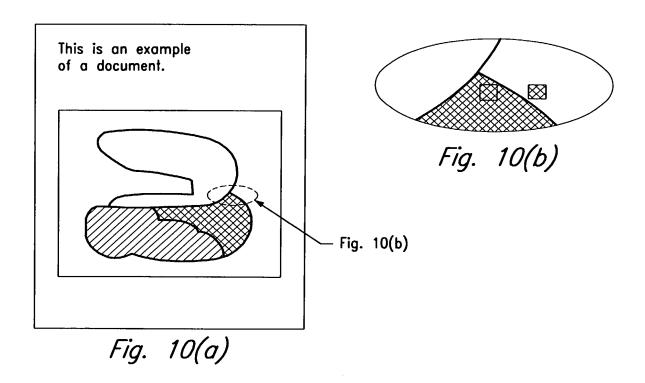


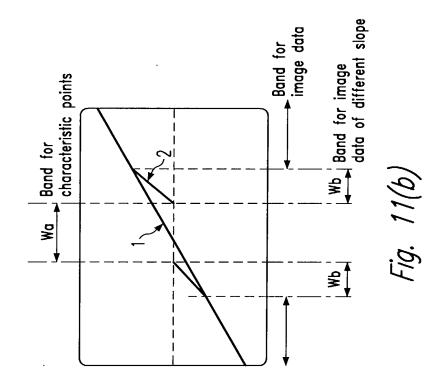




- -- Path of embedding of 1st set
- ← Path of embedding of 2nd set
- $\uparrow$  Path of embedding of 3rd set

Fig. 9





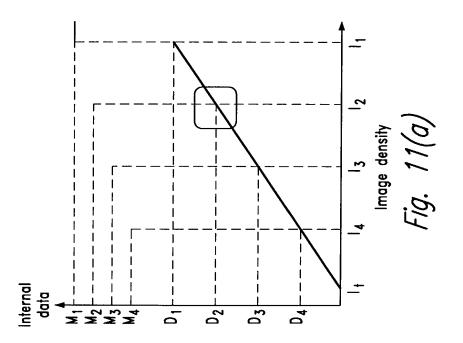




Fig. 12(a)

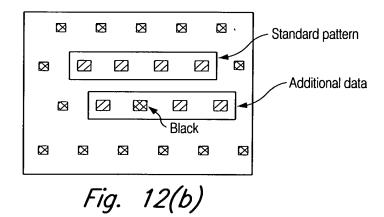
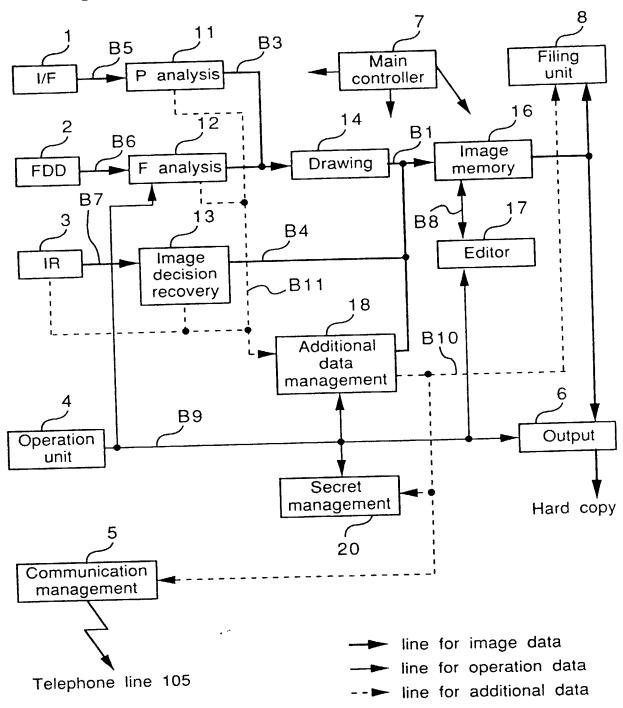
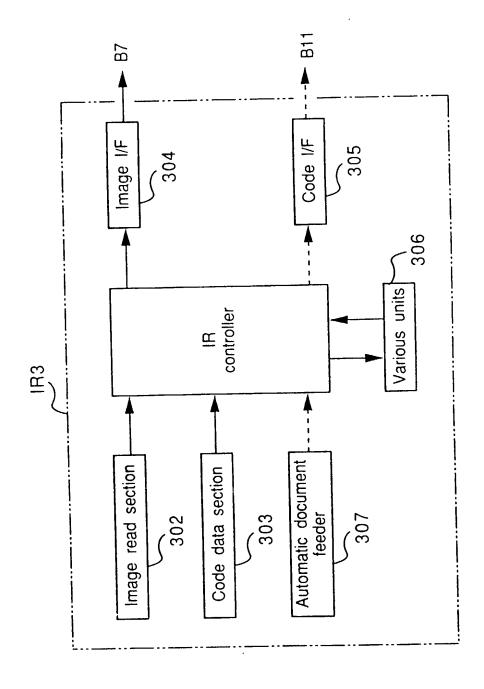
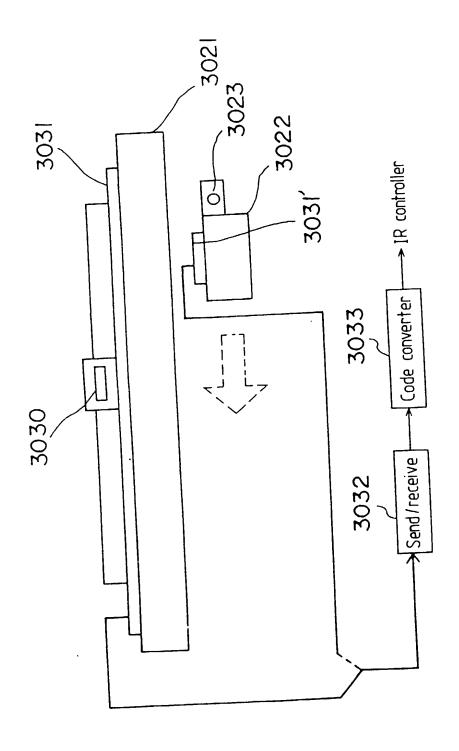


Fig. 13









F19. 1

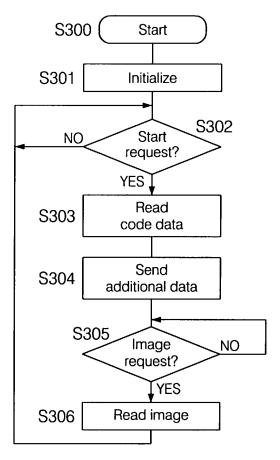


Fig. 16

407

Intrinsic data memory

Calender

IC card I/F

406

**B**3

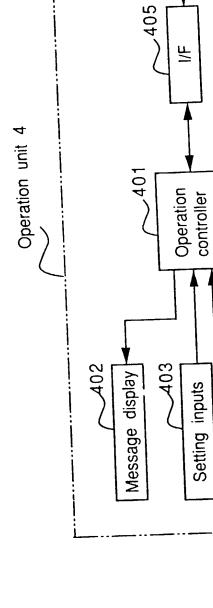


Fig. 17

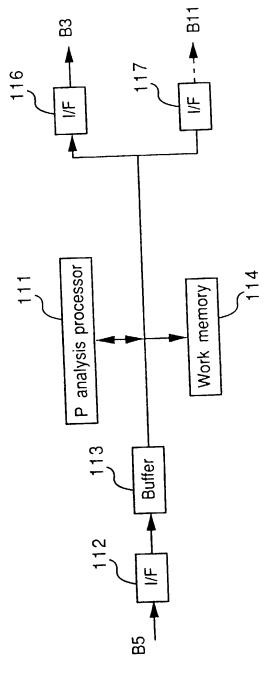
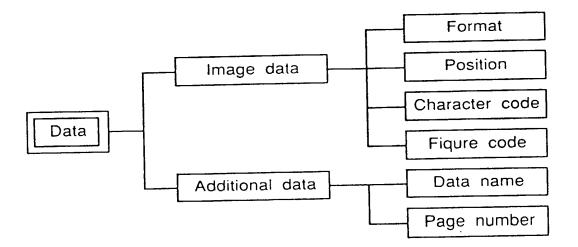
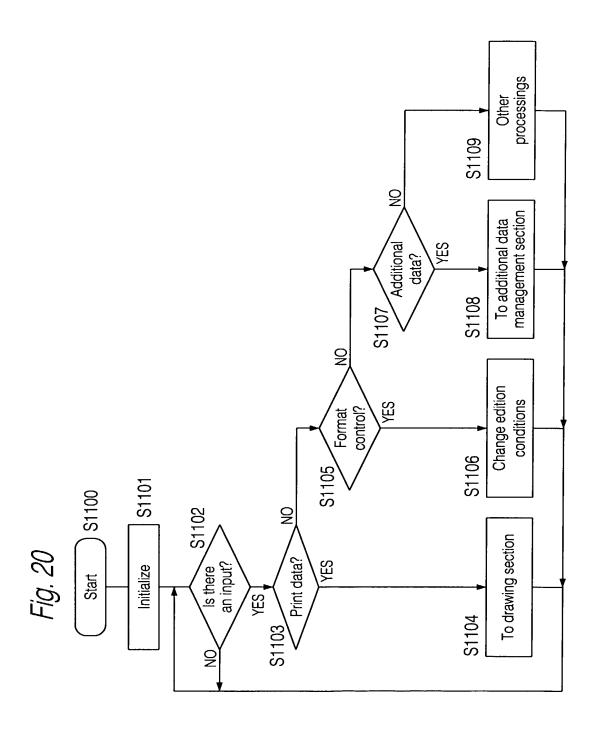


Fig. 18

Fig. 19





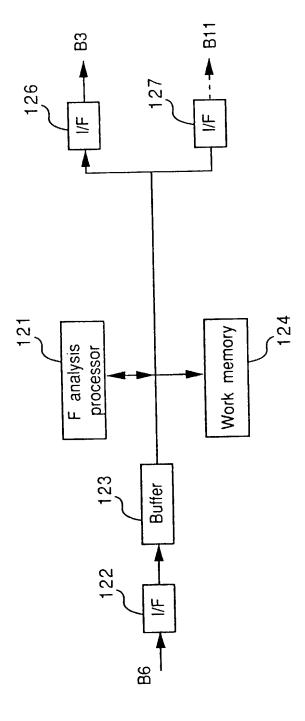
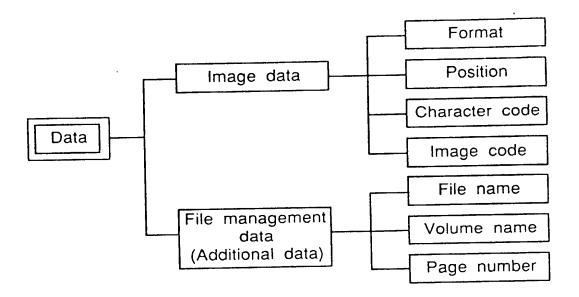
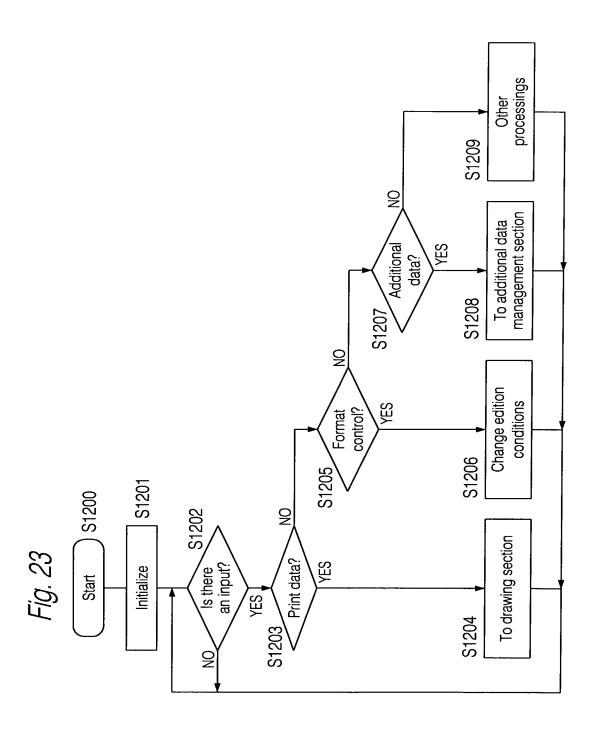
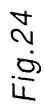


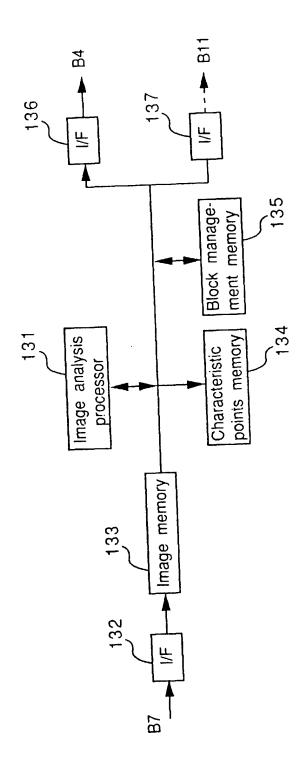
Fig.21

Fig.22









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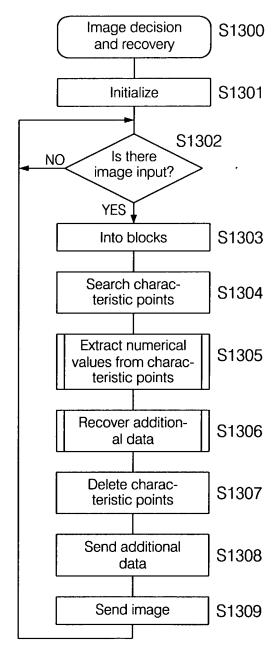


Fig. 25

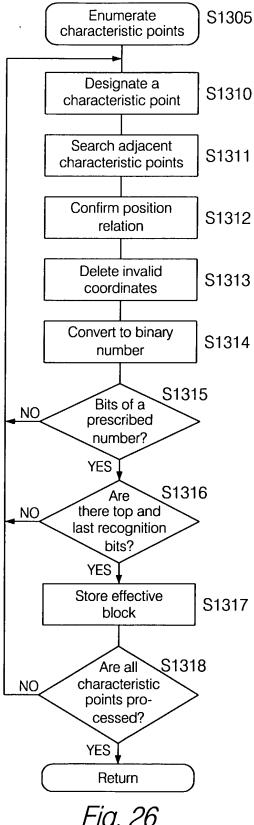
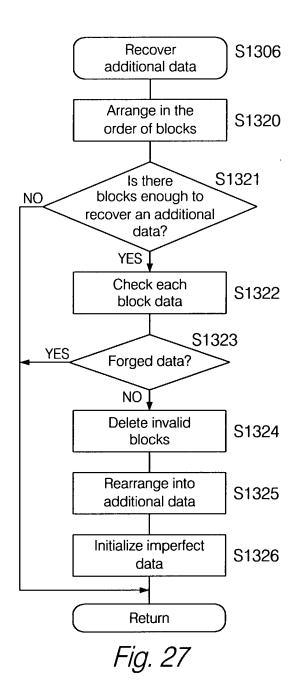
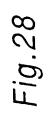
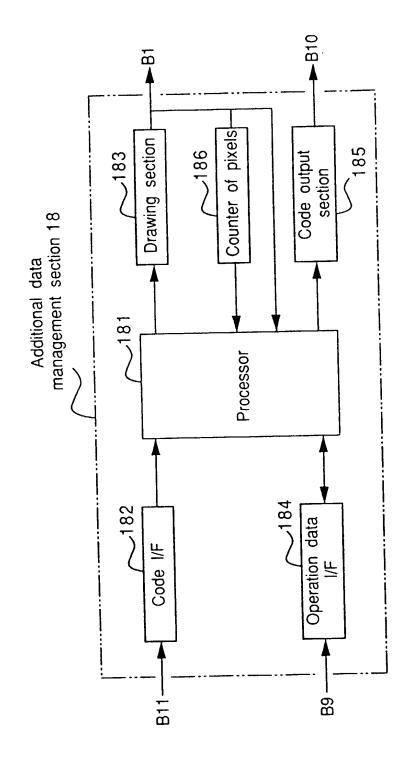


Fig. 26







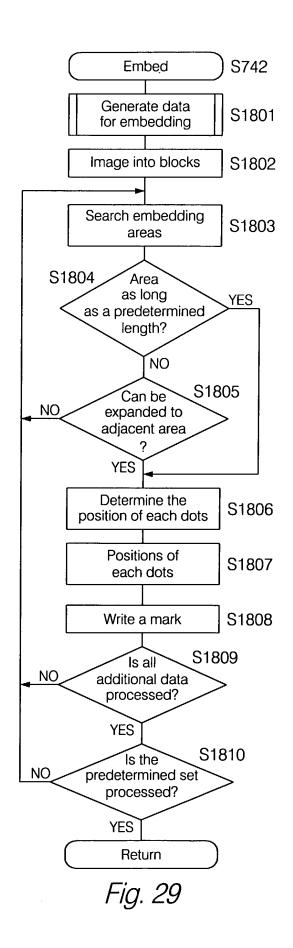


Fig.30

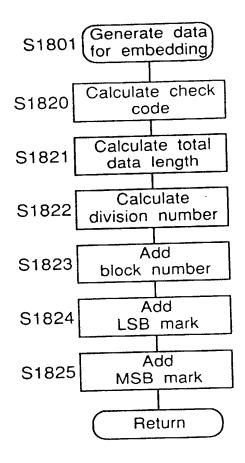
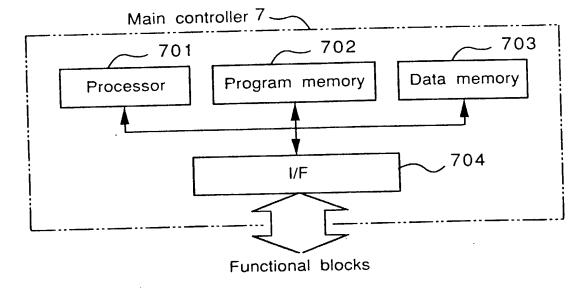
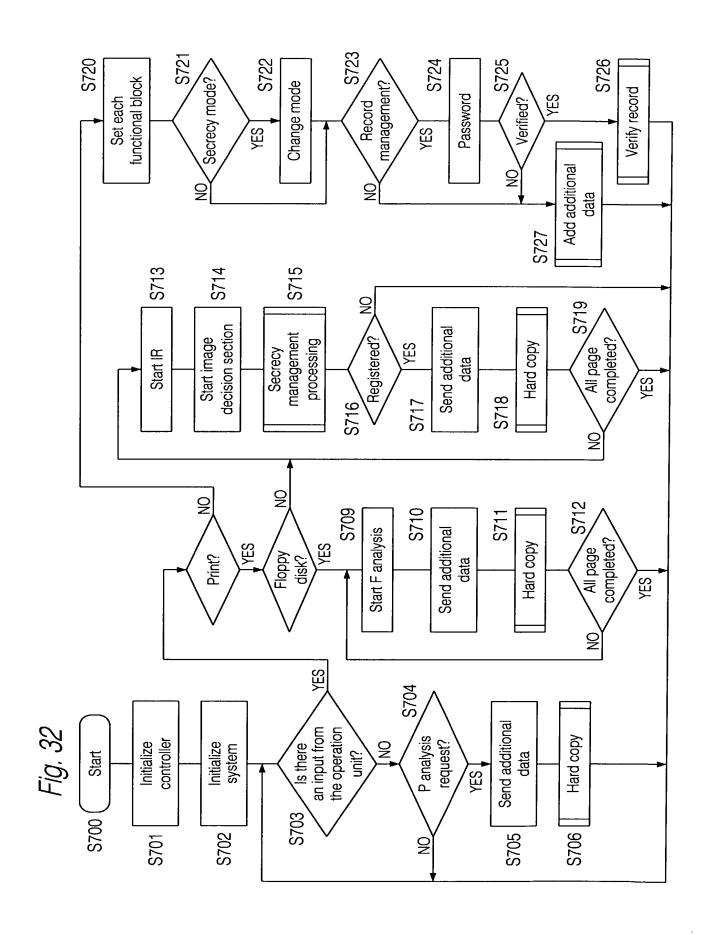
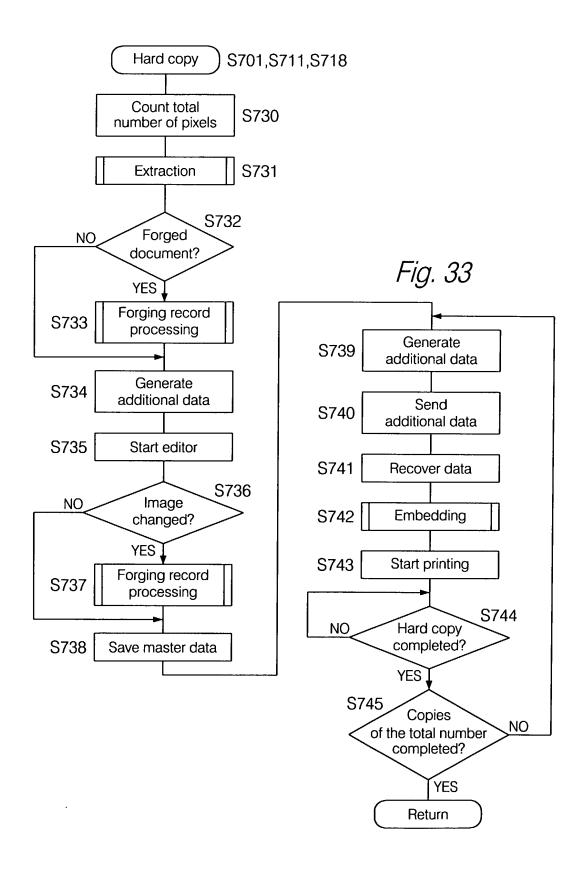
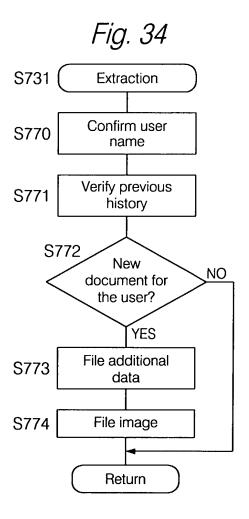


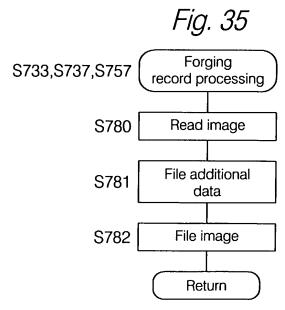
Fig.31

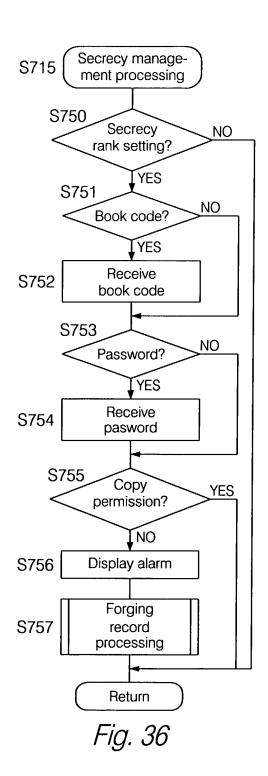


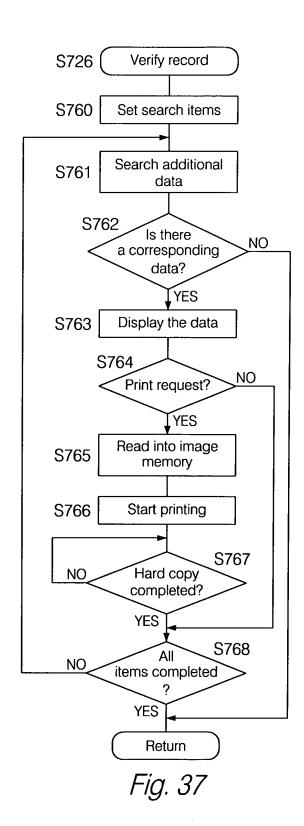


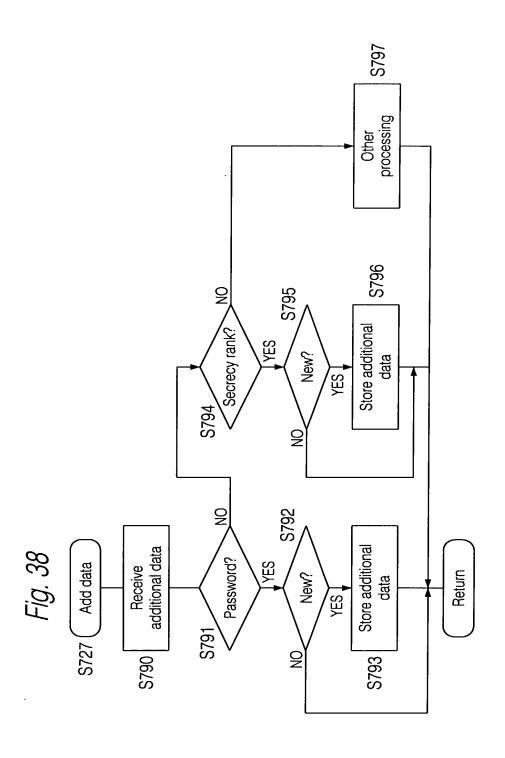


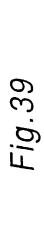


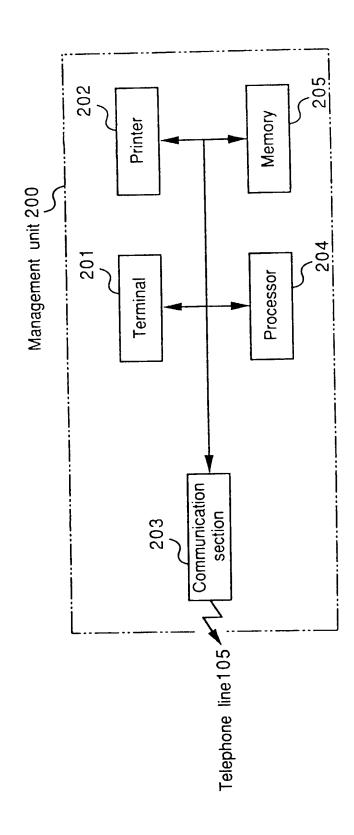




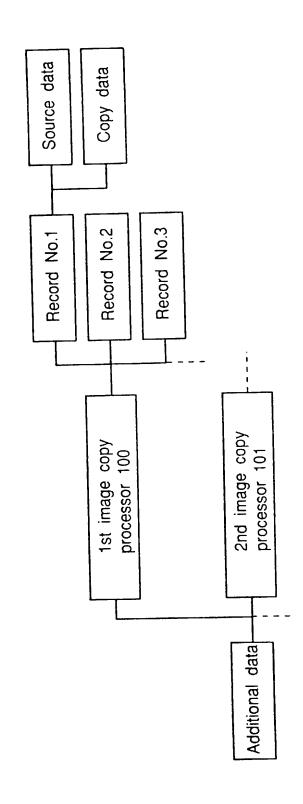












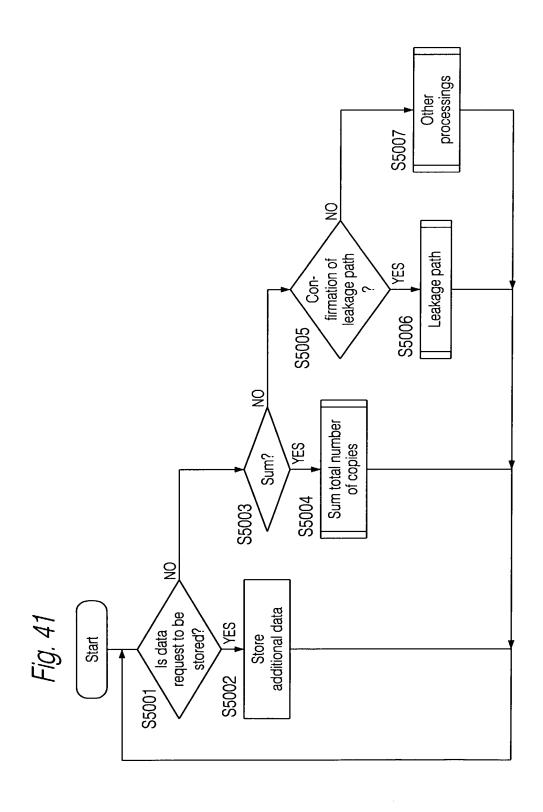


Fig.42

